

**Department of Electronics and Communication Engineering**  
**Timetable for Third Semester (Regular & Lateral), AY 2023-24**  
**(L: Lecture, T: Tutorial, P: Practical)**

Time → Day ↓	9 am -10 am	10 am -11 am	11 am - 12 pm	12 pm - 1 pm	1 pm - 2 pm	2 pm - 3 pm	3 pm - 4 pm	4 pm - 5 pm
MON	Electronic Devices and Circuits (EC2304) L Room 204	Signals and Systems (EC2307) L Room 204	LAUC (MA2302) L (LE) ODEMC (MA2301) L Room 307 & 301	<b>R E C E S S</b>	Digital System Design (EC2302) L Room 204	Professional Laws, Ethics, Values and Harmony (MG2301) L Room 305	Foundation of Physics (PH2301) L (LE) Room 101	
	Foundation of Physics (PH2301) L (LE) Room 307	LAUC (MA2302) L (LE) ODEMC (MA2301) L Room 307 & 301	Innovation and Creativity (HS2303) L Room 209/ SIC Hall		Electronic Devices and Circuits (EC2304) L Room 204	Signals and Systems (EC2307) T Room 204	Circuit Simulation Lab (EC2301) P ECE LAB 4	
LAUC (MA2302) L (LE) Room 303	Sensors and Automation (EE2310) L Room 204	Electronic Devices and Circuits (EC2304) L Room 204	Network Synthesis and Analog Filters (EC2306) L Room 204		Digital System Design Lab (EC2303) P ECE LAB 2			
Network Synthesis and Analog Filters (EC2306) L Room 204	Digital System Design (EC2302) L Room 204	LAUC (MA2302) L (LE) Room 304	Foundation of Physics (PH2301) L (LE) ODEMC (MA2301) T Room 307 & 104		Electronic Devices and Circuits Lab (EC2305) P ECE LAB 1			
Signals and Systems (EC2307) L Room 204	LAUC (MA2302) T (LE) Room 307	Network Synthesis and Analog Filters (EC2306) L Room 204	Digital System Design (EC2302) L Room 204		Sensors and Automation (EE2310) P EE Dept. Lab			

Sl.	Name of the Course (Course Code)	Faculty in-charge
1.	Linear Algebra and Univariate Calculus (LAUC) (MA2302)	Dr. P Ramwungzan
2.	Ordinary Differential Equations and Multivariate Calculus (ODEMC) (MA2301)	Huidrom Malemnganbi
3.	Foundation of Physics (PH2301)	Dr. Angom Devadatta Mani
4.	Professional Laws, Ethics, Values and Harmony (MG2301)	Dr. N. Sanatombi Devi
5.	Innovation and Creativity (HS2303)	M.C. Fujica
6.	Sensors and Automation (EE2310)	Ngayaiwon Hashunao
7.	Circuit Simulation Lab (EC2301)	Dr. Biraj Shougaijam

Sl.	Name of the Course (Course Code)	Faculty in-charge
8.	Digital System Design (EC2302)	Pinky Khundrakpam
9.	Digital System Design Lab (EC2303)	Pinky Khundrakpam
10.	Electronic Devices and Circuits (EC2304)	Thingbaijam Rajkumari Chanu
11.	Electronic Devices and Circuits Lab (EC2305)	Thingbaijam Rajkumari Chanu
12.	Network Synthesis and Analog Filters (EC2306)	Dr. Biraj Shougaijam
13.	Signals and Systems (EC2307)	Yengkhom Chandrika Devi

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**Department of Electronics and Communication Engineering**  
**Timetable for Fifth Semester, AY 2023-24**  
**(L: Lecture, T: Tutorial, P: Practical)**

<i>Time →</i> <i>Day ↓</i>	9 am -10 am	10 am -11 am	11 am - 12 pm	12 pm - 1 pm	1 pm - 2 pm	2 pm - 3 pm	3 pm - 4 pm	4 pm - 5 pm
<b>MON</b>	Configurable Logic and Processor Design (EC3501) L	Probability and Statistics for Engineers (MA3501) T	Digital Communication Systems (EC3503) L	<b>R E C E S S</b>	English Language Proficiency (HS3504) L	Digital Signal Processing Lab (EC3506) P		
	Room 203	Room 307	Room 203		Room 301	ECE LAB 4		
<b>TUE</b>	R and Python Programming (EC3509) P		Probability and Statistics for Engineers (MA3501) L		Digital Signal Processing (EC3505) L	Electromagnetic Waves and Antennas (EC3507) L	Configurable Logic and Processor Design (EC3501) L	
	ECE LAB 4		Room 307		Room 203	Room 203	Room 203	
<b>WED</b>	Constitution of India (HS3505) L	Electromagnetic Waves and Antennas (EC3507) L	Digital Communication Systems (EC3503) L		Data Analytics (CS3528) L	Digital Signal Processing (EC3505) L	Configurable Logic and Processor Design Lab (EC3502) P	
	Room 305	Room 203	Room 203		Room 203	Room 203	ECE LAB 4	
<b>THU</b>	Entrepreneurship Principles and Process (MG3502) L	Configurable Logic and Processor Design (EC3501) L	Digital Communication Systems (EC3503) L		English Language Proficiency (HS3504) L	Data Analytics (CS3528) P		
	Room 305	Room 203	Room 203		Room 301	CSE Dept. Lab		
<b>FRI</b>	Probability and Statistics for Engineers (MA3501) L	Electromagnetic Waves and Antennas (EC3507) L	Digital Signal Processing (EC3505) L		Configurable Logic and Processor Design (EC3501) T	Digital Communication Systems Lab (EC3504) P		
	Room 304	Room 203	Room 203		Room 203	ECE LAB 3		

Sl.	Name of the Course (Course Code)	Faculty in-charge
1.	Configurable Logic and Processor Design (EC3501)	Waikhom Mona Chanu
2.	Configurable Logic and Processor Design Lab (EC3502)	Waikhom Mona Chanu
3.	Digital Communication Systems (EC3503)	Rosy Kimneithem Haokip
4.	Digital Communication Systems Lab (EC3504)	Dr. Biraj Shougaijam
5.	Digital Signal Processing (EC3505)	Yengkhom Chandrika Devi
6.	Digital Signal Processing Lab (EC3506)	Yengkhom Chandrika Devi
7.	Electromagnetic Waves and Antennas (EC3507)	Thingbaijam Rajkumari Chanu

Sl.	Name of the Course (Course Code)	Faculty in-charge
8.	R and Python Programming (EC3509)	Dr. Loukrakpam Merin Singh
9.	Probability and Statistics for Engineers (MA3501)	Dr. Thokchom Chhatrajit Singh
10.	Data Analytic (CS3528)	Dr. Rajkumari Roshni
11.	English Language Proficiency (HS3504)	Dr. L. Iboyaima Singh
12.	Constitution of India (HS3505)	Dr. N. Sanatombi Devi
13.	Entrepreneurship Principles and Process (MG3502)	Gogo Athokpam

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**Department of Electronics and Communication Engineering**  
**Timetable for Seventh Semester, AY 2023-24**  
(L: Lecture, T: Tutorial, P: Practical)

<i>Time →</i> <i>Day ↓</i>	9 am -10 am	10 am -11 am	11 am - 12 pm	12 pm - 1 pm	1 pm - 2 pm	2 pm - 3 pm	3 pm - 4 pm	4 pm - 5 pm	
<b>MON</b>		DEC-II Fibre Optic Communication (EC4705) L	Mobile Communication (EC4706) L	<b>R E C E S S</b>	Deep Learning and Edge Intelligence Lab (EC4702) P				
		Room 302	Room 302		CSE Dept. Lab				
<b>TUE</b>		Deep Learning and Edge Intelligence (EC4701) L	DEC-II Fibre Optic Communication (EC4705) L		Embedded Systems Design Lab (EC4704) P				
		Room 302	Room 302		ECE LAB 2				
<b>WED</b>		Mobile Communication (EC4706) L	Deep Learning and Edge Intelligence (EC4701) L		IOC-II L				
		Room 302	Room 302						
<b>THU</b>		Embedded Systems Design (EC4703) L	DEC-II Fibre Optic Communication (EC4705) L		Mobile Communication Lab (EC4707) P				
		Room 302	Room 302		ECE LAB 4				
<b>FRI</b>		Mobile Communication (EC4706) L	Embedded Systems Design (EC4703) L		IOC-II L		LLC L	Intellectual Property Rights (HS4708) L	
		Room 302	Room 302					Room 101	

Sl.	Name of the Course (Course Code)	Faculty in-charge
1.	Deep Learning and Edge Intelligence (EC4701)	Dr. Rajkumari Roshni
2.	Deep Learning and Edge Intelligence Lab (EC4702)	Dr. Rajkumari Roshni
3.	Embedded Systems Design (EC4703)	Laiphangbam Renita Devi
4.	Embedded Systems Design Lab (EC4704)	Laiphangbam Renita Devi
5.	DEC-II: Fibre Optic Communication (EC4705)	Pinky Khundrakpam

Sl.	Name of the Course (Course Code)	Faculty in-charge
6.	Mobile Communication (EC4706)	Thingujam Nirupama Devi
7.	Mobile Communication Lab (EC4707)	Thingujam Nirupama Devi
8.	Intellectual Property Rights (HS4712)	Kosygin Leishangthem
9.	Interdisciplinary Open Course-II (IOC-II)	
10.	Liberal Learning Course (LLC)	

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