



ELECTRONICS & COMMUNICATION ENGINEERING DEPARTMENT
MANIPUR TECHNICAL UNIVERSITY

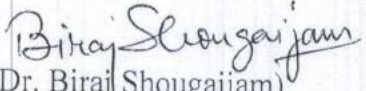
[A University established under the Manipur Technical University Act, 2016]
Takyelpat, Imphal West, Manipur-795004
Website: www.mtu.ac.in / www.mtuonline.in

NOTICE

Imphal, 10th June 2020

File No.: 1/MTU/ECE/2019/12: This is to inform to all the students of 2nd Semester (Group A), 4th semester of Electrical Engineering, 6th Semester of Computer Science and Engineering and Electrical Engineering and 4th, 6th and 8th semester of Department of Electronics and Communication Engineering that the remedial classes will commence from 10th June 2020 as per the time table attached herewith.

2. Those students having backlog in the subjects mention on time table may also join the remedial class.


(Dr. Biraj Shougaijam)
Assistant Professor and Head,
Dept. of ECE,
Manipur Technical University

Enclosure:

1. Time table for remedial classes

Copy to:

1. PS to the Vice-Chancellor, MTU (For kind information)
2. Registrar, MTU (For Kind information)
3. Equity Coordinator
4. HoD of EE and CSE Department
- ✓ 5. Web Administrator (for upload)
6. Notice Board
7. Guard File

Time table for remedial classes

2 rd Semester			
Day	Time	Subject	Faculty
Monday	2:00 - 3:00 pm	Basic Electronics Engineering	Dr. Biraj Shougaijam, Mr. Merin Loukrakpam Ms. Yengkhom Chandrika Devi Ms. Pinky Khundrakpam
Wednesday	9:00 - 10:00 am		
Thursday	9:00 - 10:00 am		

4 rd Semester			
Day	Time	Subject	Faculty
Tuesday	1:00 - 2:00 pm	Signal and Systems	Ms. Yengkhom Chandrika Devi
Friday	9:00 - 11:00 am		
Saturday	11:00-12:00 pm		
Thursday	2:00-4:00 pm	Digital Electronics (EE)	Ms. Yengkhom Chandrika Devi
Saturday	1:00-4:00 pm		
Tuesday	3:00pm-4:00pm	Pulse and Digital Circuits	Mr. Merin Loukrakpam
Wednesday	1:00pm-2:00pm		
Thursday	9:00am-10:00am		
Saturday	1:00pm-2:00pm		
Monday	1:00-3:00 pm	Analog Electronics Circuits	Dr. Biraj Shougaijam
Thursday	1:00-3:00 pm		

6 th Semester			
Day	Time	Subject	Faculty
Monday	11:00-12:00 pm	Data Communication Network	Ms. Yengkhom Chandrika Devi
Thursday	11:00-12:00 pm		
Monday	1:00 - 3:00 pm	Introduction to VLSI Design	Mr. Merin Loukrakpam
Tuesday	1:00 - 2:00 pm		
Friday	1:00 - 3:00 pm		
Tuesday	11:00-12:00 pm	Digital Signal Processing (EE, ECE and CSE)	Ms. Rosy Kimneithem Haokip
Friday	11:00-12:00 pm		

8 th Semester			
Day	Time	Subject	Faculty
Monday	10:00 am- 12:00 pm	Information Theory and coding	Mr. Langpoklakpam Catherin
Thursday	11:00 am- 12:00 pm		
Friday	1:00 pm- 1:00 pm		
Monday	10:00-12:00 pm	Digital VLSI Design	Dr. Biraj Shougaijam
Friday	1:00 pm-3:00 pm		

Biraj Shougaijam