

Tentative Time Table for 3rd semester(Electrical Engineering)

DAY	09-10 am	10-11 am	11-12 am	01-02 pm	02-03 pm	03-04 pm	04-05 pm
MONDAY	Room-1	Room-1	Room-1	Electrical Machines -I Lab.			
	Electrical Machines -I (EE2303) O M Meetei	Mathematics -III (MA2303) Dr. Th Chhatrajit	Network Analysis and Synthesis (EE2302) Rosy K. Hoakip	Electrical Machines -I Lab (EE2304) O M Meetei			
TUESDAY	Room-1	Room-1	Room-1	Lab 3	Room-1		
	Electromagnetic Theory (EE2305) Dr. RK Kamaljit	Data Structures (CS2302) Chiranjiv Ch	Network Analysis and Synthesis (EE2302) Rosy	Data Structures Lab (CS2302) Chiranjiv Ch	Analog Electronics and Circuits (EC2408) Dr. Biraj S		
WEDNESDAY	Room-1	Room-1	Room-1	Room-1	Room-1	Room-1	
	Analog Electronics and Circuits (EC2408) Dr. Biraj S	Mathematics -III (MA2303) Dr. Th Chhatrajit	Data Structures (CS2302) Chiranjiv Ch	Electrical Machines -I (EE2303) O M Meetei	Electromagnetic Theory (EE2305) Dr. RK Kamaljit	Technical Communication and Soft Skill (PC2302) Dr. L Iboyaima	
THURSDAY	Room-1	Room-1	Room-1	Room-1	Room-1	Room-1	Room-1
	Electromagnetic Theory (EE2305) Dr. RK Kamaljit	Mathematics -III (MA2303) Dr. Th Chhatrajit	Electrical Machines -I (EE2303) O M Meetei	Analog Electronics and Circuits (EC2408) Dr. Biraj S	Network Analysis and Synthesis (EE2302) Rosy K. Hoakip	Technical Communication and Soft skill (PC2302) Dr. L Iboyaima	Analog Electronics and Circuits (EC2408) Dr. Biraj S
FRIDAY	Room-1	Room-1	Room-1	Room-1	Analog Electronics and Circuits Lab.		
	Data Structures (CS2302) Chiranjiv Ch	Mathematics -III (MA2303) Dr. Th Chhatrajit	Electromagnetic Theory (EE2305) Dr. RK Kamaljit	Electrical Machines -I (EE2303) O M Meetei	Analog Electronics and Circuits Lab (EE2306) Dr. Biraj S		
SATURDAY							

Biraj S