

## Tentative Time Table for 7<sup>th</sup> semester(Electrical Engineering)

DAY	08-09 am	09-10 am	10-11 am	11-12 am	01-02 pm	02-03 pm	03-04 pm	04-06 pm
		<b>Room-13</b>	<b>Room-13</b>	<b>Room-13</b>	<b>Programmable Logic Controllers Lab.</b>			<b>Room-5/6</b>
<b>MONDAY</b>	<b>MOOCS</b>	Power Quality and FACTS (EE4725) Guest Faculty 1	Programmable Logic Controllers (EE4724) Guest faculty 2	Control system Design (EE4733) T Jeneetaa	Programmable Logic Controllers Lab (EE4724) T Jeneetaa			<b>GATE</b>
		<b>Room-13</b>	<b>Room-13</b>	<b>Room-13</b>				<b>Room-5/6</b>
<b>TUESDAY</b>	<b>MOOCS</b>	Programmable Logic Controllers (EE4724) Guest faculty 2	Electric Drives (EE4728) Guest Faculty 3	Control system Design (EE4733) T Jeneetaa	Minor project (EE4730)			<b>GATE</b>
		<b>Room-13</b>	<b>Room-13</b>	<b>Room-13</b>				<b>Room-5/6</b>
<b>WEDNESDAY</b>	<b>MOOCS</b>	Electric Drives (EE4728) Guest Faculty 3	Power Quality and FACTS (EE4725) Guest Faculty 1	Power System Analysis (EE4727)	Minor project (EE4730)			<b>GATE</b>
		<b>Room-13</b>	<b>Room-13</b>	<b>Room-13</b>	<b>Electronics Design Lab.</b>			<b>Room-5/6</b>
<b>THURSDAY</b>		Power Quality and FACTS (EE4725) Guest faculty 1	Power System Analysis(EE4727)	Control system Design (EE4733) T Jeneetaa	Electronics Design Lab (EE4726) O Manganleiba Meetei			<b>GATE</b>
		<b>Room-13</b>	<b>Room-13</b>	<b>Room-13</b>	<b>Electric Drives Lab.</b>			<b>Room-5/6</b>
<b>FRIDAY</b>		Control system Design (EE4733) T Jeneetaa	Power System Analysis (EE4727)	Power System Analysis (EE4727)	Electric Drives Lab (EE4729) O Manganleiba Meetei			<b>GATE</b>
		<b>Room-13</b>	<b>Room-13</b>	<b>Room-13</b>				<b>Room-5/6</b>
<b>SATURDAY</b>	<b>MOOCS</b>	Programmable Logic Controllers (EE4724) Guest faculty 2	Electric Drives (EE4728) Guest Faculty 3	Power Quality and FACTS (EE4725) Guest Faculty 1	<b>Training Seminar (EE4735)</b>			<b>GATE</b>

*K. Singh*