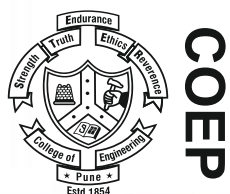# "Research Scholars' Conclave"
10th and 11th of July 2020

COEP

Organized jointly by
Departments of Electronics and Telecommunication,
Instrumentation and control, Computer and IT,
of
College of Engineering Pune,
Shri G. S. Institute of Technology & Science-Indore and
Manipur Technical University-Imphal,
sponsored by TEQIP-III,
Under the aegis of
Center of Excellence in Signal & Image Processing (CoE-S&IP)

This conclave aims at providing platform to the research scholars of our Institute and its twinning partners (mentee institutes) under TEQIP-III, viz. SGSITS-Indore and Manipur Technical University-Imphal, to present their ongoing research to the gathering of domain specific research scholars from these three associates. Broad domains identified for this conclave (but, not restricted to) matching with the themes of Center are:

- Machine / Deep learning, in Signal Processing, Computer Vision for Industrial Inspection, Speech/Audio, Biomedical Signal Processing
- Optimum Digital Control and Instrumentation for Automation
- Big data Analytics and Information Security

Besides this, we envisage to plan **Keynote sessions** by eminent speakers on state of the art research topics in this domain.

Patrons

Director, COEP Prof. B. B. Ahuja	**Director, SGSITS,** Prof. R. K. Saxena
Principal investigator, COE-SIP and Deputy Director, Prof. Mukul Sutaone	**Vice-Chancellor MTU,** Prof. Laiphrakpam Tombi Singh

Coordinators

From COEP Pune Dr. S. P. Mahajan, Dr. Mrs. Vibha Vyas Dr. V. N. Pande, Dr. D. N. Sonawane Dr. Mrs. Vahida Attar	**From SGSITS Indore** Dr. Shekhar Sharma, Dr. R. S. Gamad Dr. Urjita Thakar, Dr. Sunita Varma **From MTU Imphal** Dr. Biraj Shougaijam

Session Coordinators and Organising Committee

Dr. Radhika D. Joshi Dr. Aditya Gupta	Dr. Sunil B. Mane Prakash Chandore	Prof. Ashwin Shrivastava Dr. Sandeep Hanwate

College of Engineering Pune
in association with
SGSITS-Indore and MTU-Imphal
organises
"Research Conclave- 2020"
10th and 11th July 2020

Under TEQIP-III

Schedule: Day-1

The Keynote sessions are of ONE hour each
Research Scholar presentations are for 15 minutes each)

Sr. No.	Time Slot	Event
1	**10:00 am to 12:00 pm**	**Inauguration with Keynote Address by :** **Prof. R. K. Shevgaonkar, Vice Chancellor, Bennett University** Directors of COEP, GSITS & VC-MTU Session Coordinator: Dr.S.P.Mahajan
2	**12:30 pm to 3:00 pm** (12:30 to 1:30, Keynote 1:30 to 3:00 Presentations)	Slot for E&TC- COEP PhD scholar Presentations **Keynote by**: **Prof. Biplab Banerjee, IIT Powai)** Session Coordinator: Dr.Mrs. Radhika Joshi
3	**3:00 pm to 5:30 pm** (3:00 to 4:00, Keynote 4:00 to 5:30 pm Presentations)	Slot for Computer Engg. COEP PhD scholar Presentations **Keynote by: Ajay Chebbi, Chief Architect, IBM, Pune Member, IBM Cloud Platform**) Session Coordinator: Dr. Sunil B.Mane

Schedule: Day-2

Sr. No.	Time Slot	Event
1	**10:00 am to 12:30 pm** (10:00 to 11:00, Keynote 11:00 to 12:30 Presentations)	Slot for Instru-COEP PhD scholars Presentations and **Keynote by: Prof. Radhakant Padhi, IISc Bangalore** Session Coordinator:Dr. Sandeep Hanwate
2	**12:30 pm to 3:00 pm** (12:30 to 1:30, Keynote 1:30 to 3:00 Presentations)	Slot for GSITS PhD scholar Presentations and **Keynote by: Prof. Vimal Bhatia, IIT Indore** Session Coordinator: Prof. Ashwin Shrivastava
3	**3:30 pm to 5:30 pm** (3:30 to 4:30 pm: Keynote 4:30 to 5:30: Panel discussion as a part of valedictory function)	**Valedictory Function:Keynote by:** **Prof. M.S. Sutaone, COEP** Panel Discussion Session Coordinator: Dr. Vibha Vyas