



MANIPUR TECHNICAL UNIVERSITY

[A University established under the Manipur Technical University Act, 2016]

Takyelpat, Imphal West, Manipur-795004

Website: www.mtu.ac.in / www.mtuonline.in

NOTICE

Imphal, 22nd August, 2019

No. 17/27/2019-MTU: This is to notify all the faculties and students that the Department of Electronics and Communication Engineering, MTU, will be organizing a five (5) days' workshop on "**Recent Trends in Microelectronics and VLSI Technology**" from **26th to 30th August 2019** under the sponsorship of **TEQIP-III, MHRD, Govt. of India** at the conference room.

2. All the interested faculties and students are requested to attend the workshop.


(Dr. Th Chhatrajit Singh)
Academic Coordinator

Copy to:

1. P.S to the Vice Chancellor (For kind information)
2. Registrar (For kind information)
3. Asst. Registrar (Academic)
4. All the faculties
- ✓ 5. Web Administrator (for upload)
6. Notice Board
7. Guard File

Schedule for five (5) days' workshop on "Recent Trends in Microelectronics and VLSI Technology"
from 26th to 30th August 2019

	10am - 11am	11am - 12pm	12pm - 1pm	1pm - 2pm	2pm - 2:30pm	2:30pm - 3:30pm
26-Aug	Inauguration	Session 1	Lunch Break	Session 2	Tea Break	Session 2
27-Aug	Session 3		Lunch Break	Session 4	Tea Break	Session 4
28-Aug	Session 5		Lunch Break	Session 6	Tea Break	Session 6
29-Aug	Session 7		Lunch Break	Session 8	Tea Break	Session 8
30-Aug	Session 9		Lunch Break	Session 10	Tea Break	Validictory Function

Session	Resource Person	Tentative Topic
Session 1 & 2	Mr. UttamKumar Ningthoujam, Elvego Circuits Pvt. Ltd. Bangalore	Analog and Digital VLSI Design Flow
Session 3	Dr. Anup Dandapat, NIT Meghalaya	Low power Low cost Memory Design
Session 4	Dr. T.R Lenka, NIT Silchar	Modelling and Simulation of Microelectronics Devices
Session 5	Mr. Imopishak Thingom, NIELIT Imphal	Recents Trends in VLSI Design
Session 6	Dr. Ashish Ranjan, NIT Manipur	Low power VLSI Design
Session 7	Dr. Nagesh Ch., IIT Manipur	MEMS Devices for Biomedial Applications
Session 8	Dr. Gaurav Saxena, IIT Manipur	Devices for Biomedical Applications
Session 9 & 10	Dr. Soumyajit Poddar, IIT Guwahati	AI Hardware for VLSI and Embedded Systems

K. Singh 22/8/19