



RESULT OF THE END TERM EXAMINATION, DECEMBER 2018
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

3RD SEMESTER

		MATHEMATICS-III	DATA STRUCTURE	NETWORK ANALYSIS AND SYNTHESIS	ELECTROMAGNETIC THEORY	DIGITAL ELECTRONICS	ELECTRONIC DEVICES AND CIRCUITS	ELECTRONIC DEVICES AND CIRCUITS LAB	DIGITAL LOGIC DESIGN LAB
		MA2303	CS2302	EE2302	EE2305	EC2302	EC2304	EC2305	EC2303
SL. NO	REGD. NO	GRADE	GRADE	GRADE	GRADE	GRADE	GRADE	GRADE	GRADE
1	20170100176	C	B	C	D	B	B+	A+	A+
2	20170100164	D	C	F	F	D	D	A	A
3	20180100246	D	C	F	F	D	D	A+	A
4	20180100361	F	F	F	F	D	D	A	A
5	20180100251	D	F	C	F	C	B+	A+	A
6	20160100086	F	D	F	I	D	F	A	A
7	20180100254	C	D	D	F	B	B	A+	A+
8	20170100143				D				
9	20160100084	I	I	I					
10	20170100158				D				
TOTAL NO. OF STUDENTS APPEARED		7	7	7	8	7	7	7	7
TOTAL NO. OF PASSED STUDENTS		5	5	3	3	7	6	7	7
TOTAL NO. OF FAILED STUDENTS		2	2	4	5	0	1	0	0
TOTAL NO. OF ABSENTEES		1	1	1	1	0	0	0	0

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